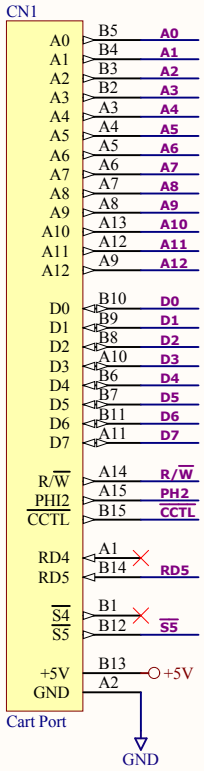

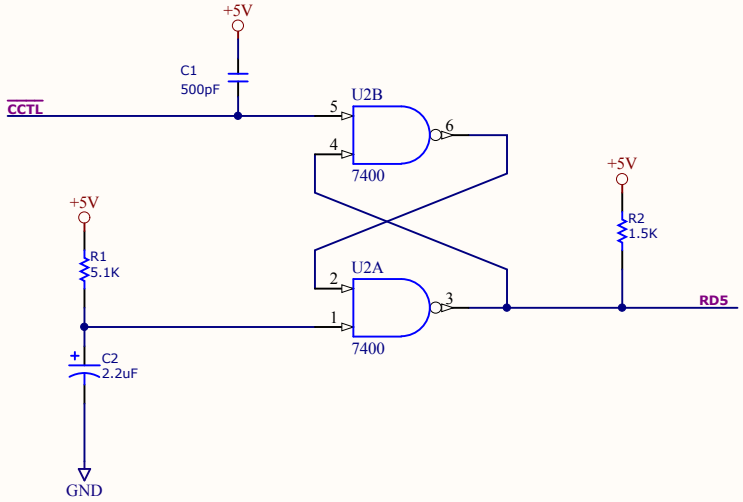

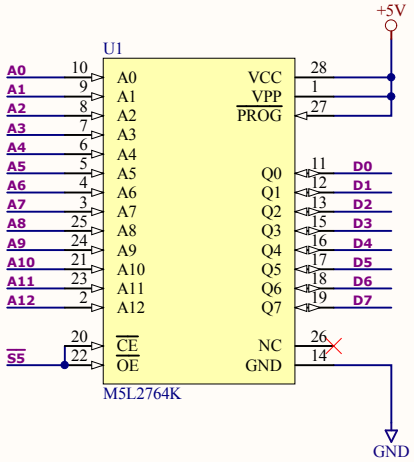

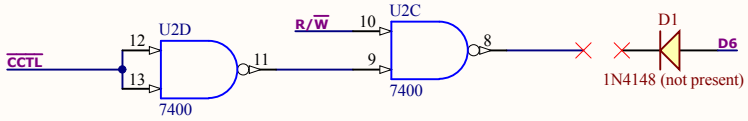

Hardware "Copy Protection"* circuit --> present but not connected in this cartridge

(*) This piece of NAND gates is pulling down the D6 bit of data bus, when any read operation is done at $D500-$D5FF area.

Software procected by this circuit, reads periodically or in random way the $D5xx area, then checks that bit D6 is always set to zero, when logic "1" occurs that means that protection circuit doesn't exist.

Title: Turbo ROM Plus(3) Cartridge		
Size: A4	Number: (rev. eng. from real hardware)	Revision: 1.0
Date: 2019-02-09		Sheet 1 of 1
File: F:\[projects]\..\TurboROM_plus(3).SchDoc		Drawn By: Seban / Slight